Job Description

Astek has a position for a Hardware Engineer that can design and develop 93K Tester Cards at both the Packaged Die and Wafer Probe level.

Ideal Candidate Will Have:
- BSEE degree
- 5 Years minimum experience in IC Test Engineering
- High-Speed PCB Design and Layout experience
- Experience testing and/or characterizing at the wafer level
- Experience writing and using test programs for IC package test
- Good background in both digital and analog test methodology
- Good understanding of high-speed PLL and Serdes operation
- Experience writing programs and/or using the Verigy/Advantest 93K Test Platform or something similar
- Familiar with test methodologies for IC's operating in the gigahertz speed range
- Experience using Labview or similar type test software
- Ability to set priorities, work independent and achieve overall program goals
- Effective communication skills and ability to work with a variety of technical professionals
- Strong documentation skills
- Broad knowledge of basic lab equipment (scopes, logic analyzers, etc) and excellent debug skills

Candidates should have a minimum of a four year technical degree and five years experience. Candidate must have US citizenship or current authorization to work in the US. Interested applicants should apply online and include a short cover letter describing your background and desired salary level as well as any points of emphasis that you feel would make you an ideal candidate for this position.
Candidate can work remotely, however, must have the ability to meet in Colorado Springs area for Test and Integration tasks.

Salary

Job Type   Employee

Job Status   Full Time

Apply Method
Standard Apply: jobs@astekservices.com

Phone :

Fax :

Email : jobs@astekservices.com