

Job Description

Digital ASIC Design and Verification Engineer

Position Summary

This position allows you to work remotely. The Digital Design and ASIC Verification Engineer (ASIC Engineer) will create module level RTL blocks and perform verification using directed test vectors along with utilizing languages such as System C or System Verilog. The ASIC Engineer works collectively within the ASIC team and will switch between writing production RTL and performing verification tasks to support the design and development of next generation system on chip (SoC).

Essential Functions and Responsibilities

- The ASIC Engineer will write module level RTL that is intended to be synthesized.
- The ASIC Engineer will have to perform module and top-level type verification tasks utilizing System C or System Verilog.
- The ASIC Engineer needs very good debug and team building skills .

Responsibilities include:

- Development of production RTL
- Verification of CMOS high-speed digital logic.
- Developing new verification IP.
- Writing and running tests to improve measured coverage.
- Adding assertions and coverage statements to verification IP.
- Additional desire for performing hands-on lab work for silicon validation, debugging, characterization, and bring up.

Competencies

- Verilog RTL Design
 - Technical Skills
 - Solid understanding of writing RTL that can be targeted towards synthesis or block level verification,
 - Object oriented class-based test bench methodologies such as VMM, OVM or UVM
 - Development of constrained pseudo-random test benches
 - Working knowledge of assertion and coverage based verification techniques
 - Experience with HDL simulators and verification tools
 - HDL debugging experience
 - Oral Communication - Speaks clearly and persuasively in positive or negative situations.
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- Written Communication - Writes clearly and informatively. Good documentation skills are a must.
- Teamwork - Contributes to building a positive team spirit.

Tools

Experience with the following is a plus:

- VCS
- UVM
- Linux Platforms
- RTL design input using Verilog in large gate type designs
- BSEE or equivalent required;
- 5+ years industry experience preferred, internships acceptable
- Strong debug skills from component level to system level
- High-Speed digital design experience
- Strong verbal and written communication skills. Technical reports and presentations of test results required.

Candidate must have US citizenship or current authorization to work in the US. Astek cannot provide support for Visa applications. Remote working will be allowed. Interested candidates should provide a resume along with a cover letter highlighting your background, skill, training and education.

Salary

Job Type Employee

Job Status Full Time

Apply Method

Standard Apply: jobs@astekservices.com

Phone :

Fax :

Email : jobs@astekservices.com
