

## Job Description

### **Position Summary**

The Design For Test (DFT) ASIC Engineer will work within an ASIC team to support both engineering development and manufacturing test. Ability to work remotely from your house.

### **Essential Functions and Responsibilities**

- Develop overall validation and manufacturing test architecture for the ASIC,
- Recommend and develop test structures within an ASIC,
- Develop both engineering verification tests and manufacturing tests,
- Create performance metrics to verify both engineering and manufacturing test coverage,
- Very good script writing skills in a Linux environment to automate processes,
- Very good RTL skills,

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### **Responsibilities include:**

- Verification of CMOS high-speed logic, digital logic.
- Developing new test and validation IP.
- Writing and running tests to improve measured coverage.
- Adding assertions and coverage statements to verification IP.
- Additional desire for performing hands-on lab work for silicon validation, debugging, characterization, and bring up.

### **Competencies**

- Design - Applies design principles; Demonstrates attention to detail.
  - Technical Skills - Strives to continuously build knowledge and skills.
    - Good understanding of object oriented verification languages such as System Verilog
    - Object oriented class-based test bench methodologies such as VMM, OVM or UVM
    - Development of constrained pseudo-random test benches
    - Working knowledge of assertion and coverage based verification techniques
    - Experience with HDL simulators and verification tools
    - HDL debugging experience
  - Oral Communication - Speaks clearly and persuasively in positive or negative situations.
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- Written Communication - Writes clearly and informatively. Good documentation skills are a must.
- Teamwork - Contributes to building a positive team spirit.

## Tools

Ideal candidate will have the following:

- VCS
- UVM
- Linux Platforms
- RTL design input using Verilog in 100 million gate type designs
- BSEE or equivalent required; MSEE preferred
- 5+ years industry experience preferred, internships acceptable
- Strong debug skills from component level to system level
- RTL design experience is a plus
- Knowledge of PCIe, DDR and AXI protocols is desirable
- Strong verbal and written communication skills. Technical reports and presentations of test results required.

Candidate must have US citizenship or current authorization to work in the US. Astek cannot provide support for Visa applications. Remote working is an option. Interested candidates should provide a resume along with a cover letter highlighting your background, skill, training and education.

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## Salary

**Job Type** Employee

**Job Status** Full Time

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## Apply Method

Standard Apply: [jobs@astekservices.com](mailto:jobs@astekservices.com)

Phone :

Fax :

Email : [jobs@astekservices.com](mailto:jobs@astekservices.com)

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